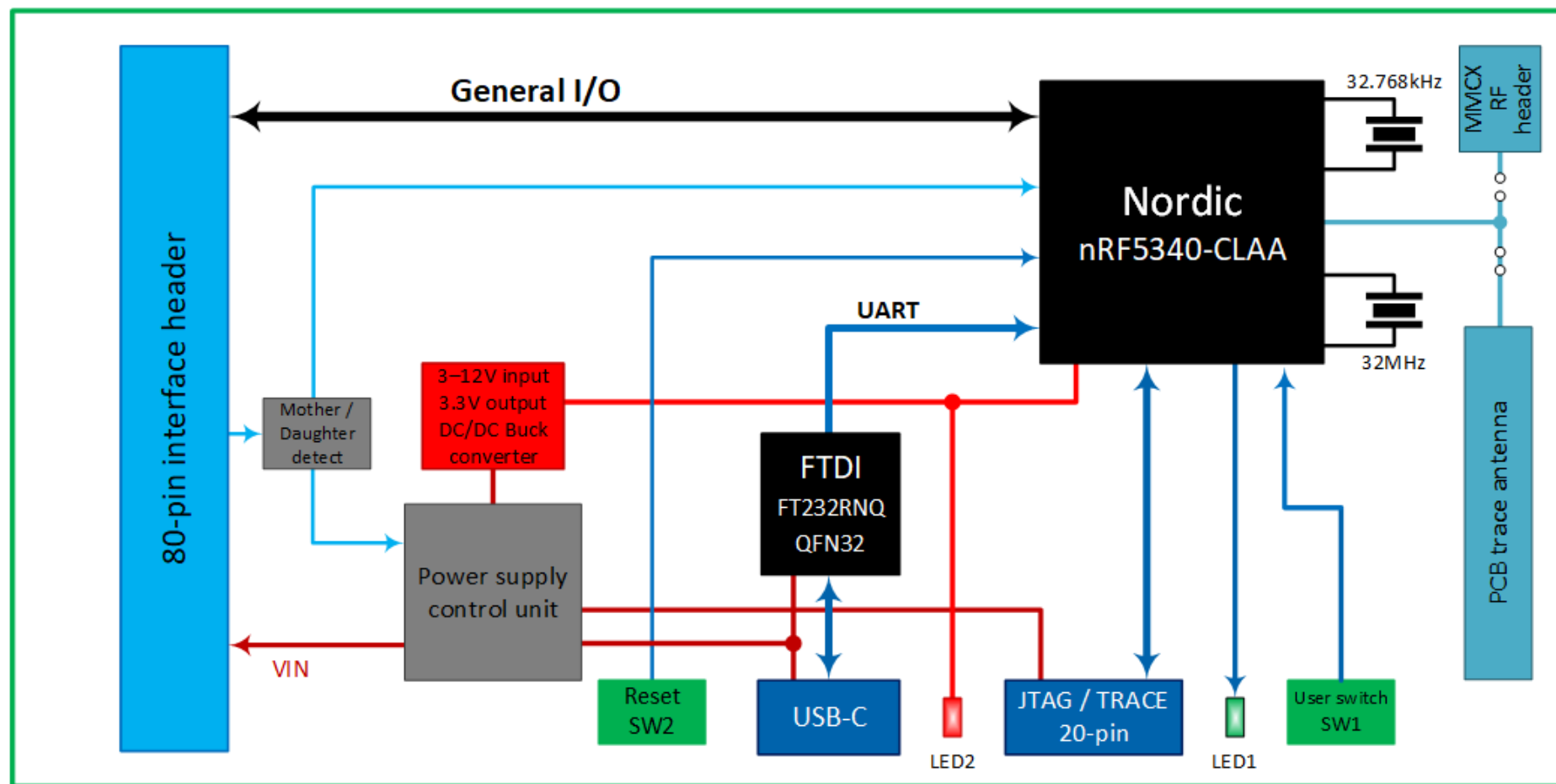




A

A

B

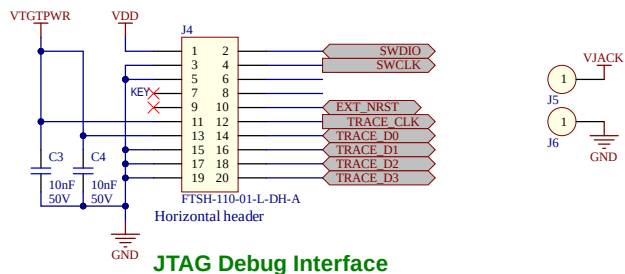
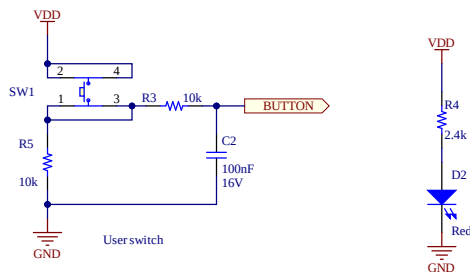
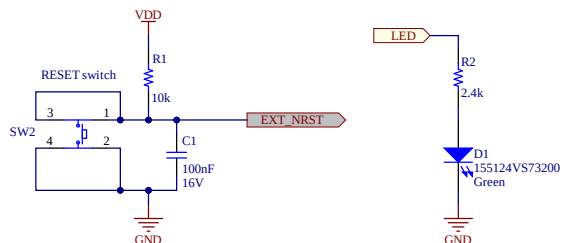
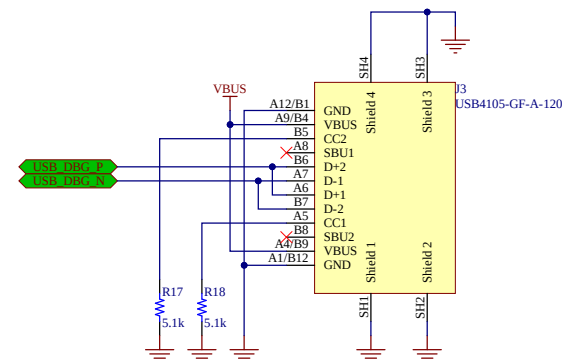
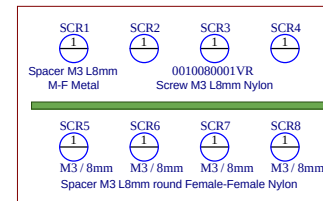
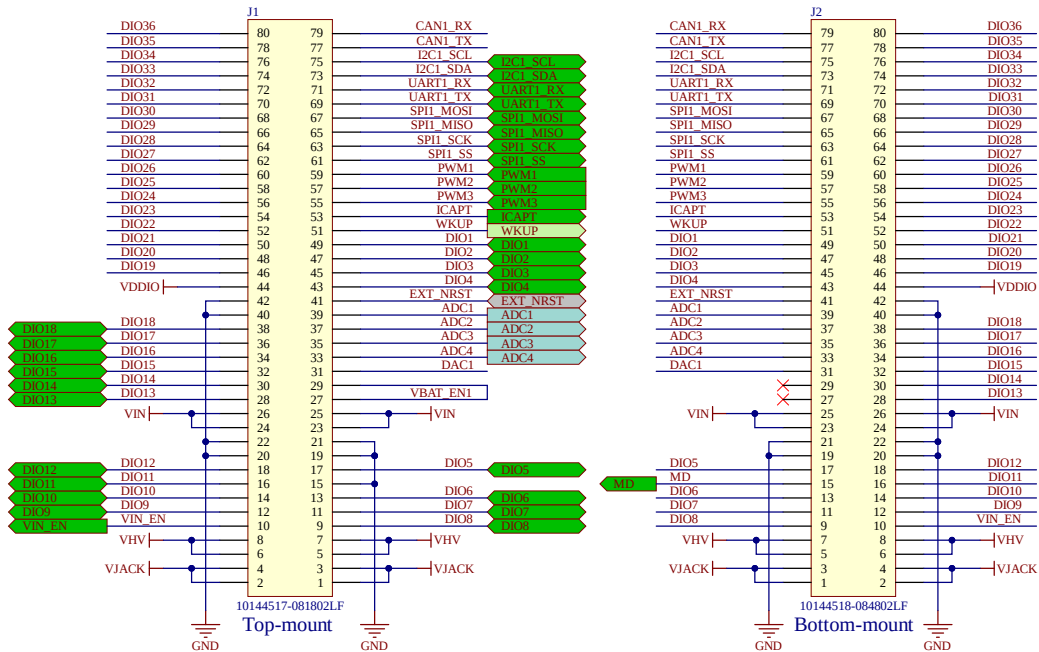
B

C

C

D

D

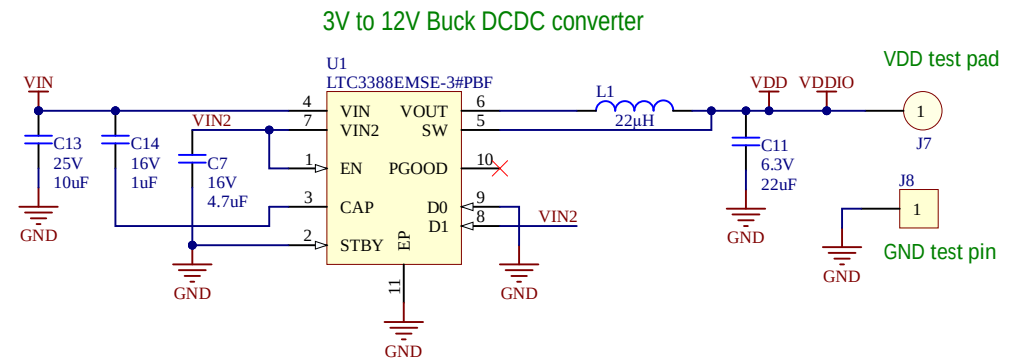
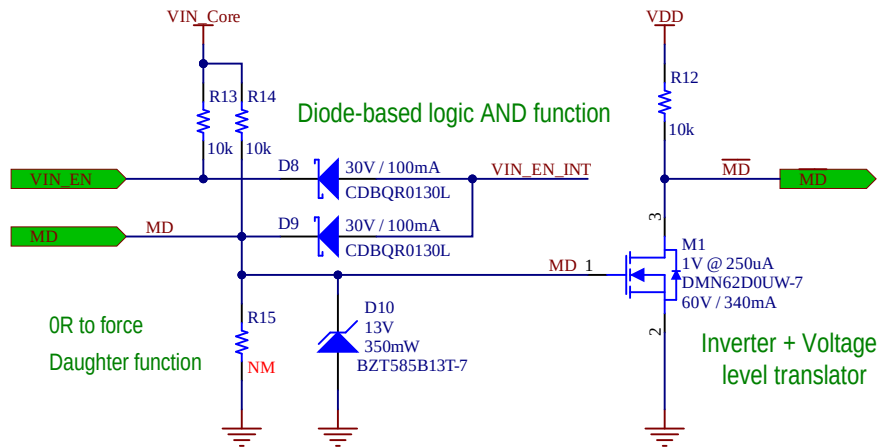
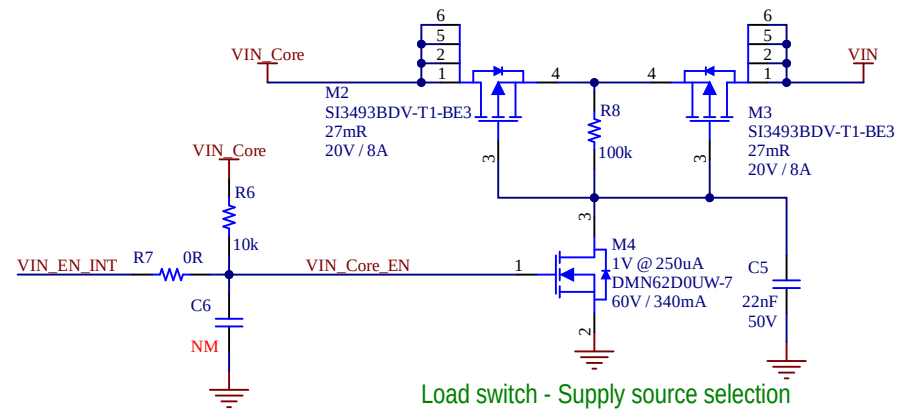
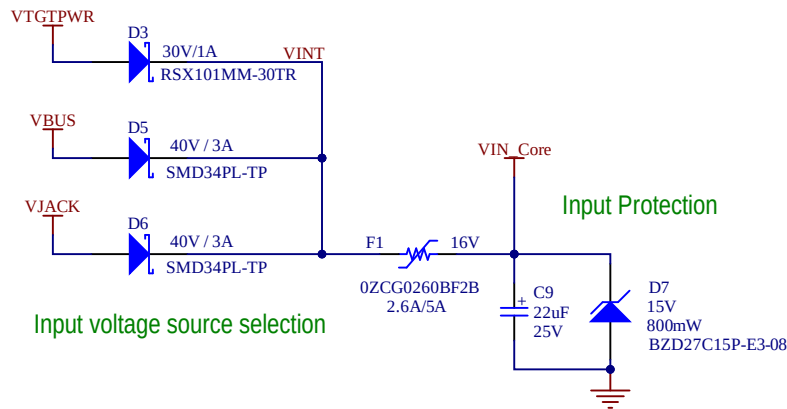


Signal	Ra	Rb	Pin
P0.28	X*		DIO5
		X	MD

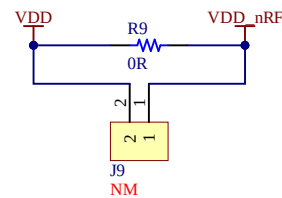
* : By default

Title: Interconnect	Version: 1.0.0
Board: Zest_Core_nRF5340-CLAA	Size: A3
Project: 6TRON	Sheet: 2 of 5
Author: SGA	Date modified: 28/03/2024

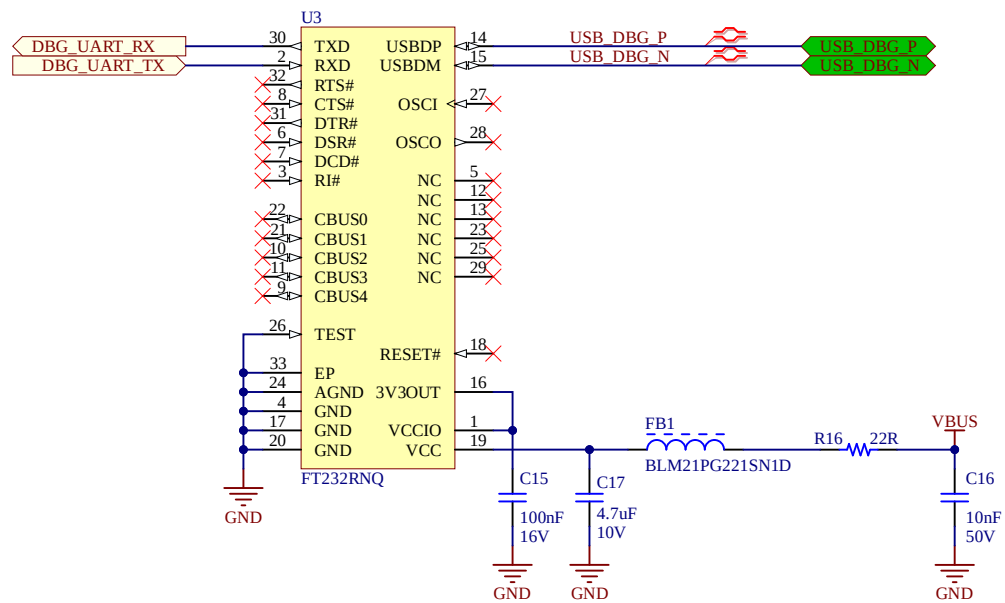




Test pad for power measurement



Title: <i>Power</i>	Version: <i>1.0.0</i>	
Board: <i>Zest_Core_nRF5340-CLAA</i>	Size: <i>A4</i>	
Project: <i>6TRON</i>	Sheet: <i>4 of 5</i>	
Author: <i>SGA</i>	Date modified: <i>27/03/2024</i>	



Title: USB	Version: 1.0.0	
Board: Zest_Core_nRF5340-CLAA	Size: A4	
Project: 6TRON	Sheet: 5 of 5	
Author: SGA	Date modified: 27/03/2024	